Basic Elements

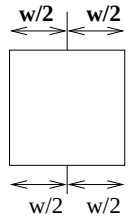

Figure 1: 1-input Element Layout

$$w(s) \quad = \quad h(w) = b(w) = \sqrt{C(s)}$$

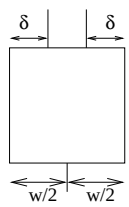

Figure 2: 2-input Element Layout

$$w(s) \quad = \quad h(w) = b(w) = \sqrt{C(s)}$$

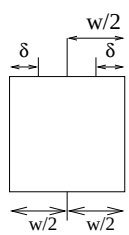

Figure 3: 3-input Element Layout

$$w(s) \quad = \quad h(w) = b(w) = \sqrt{C(s)}$$

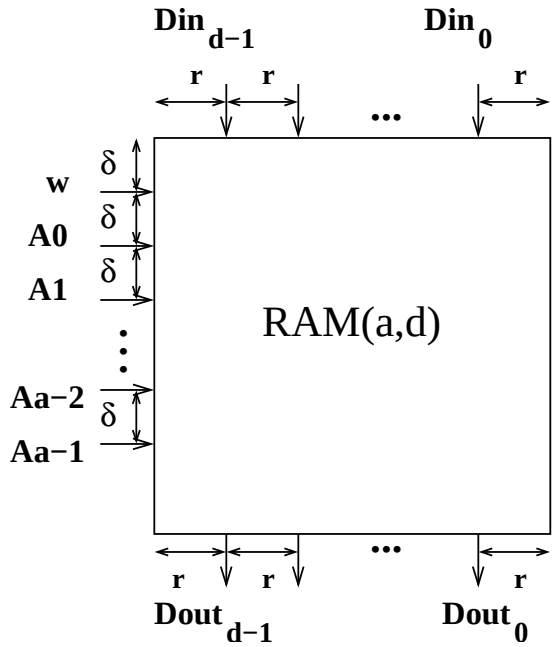

Figure 4: RAM Layout

w-write signal

$$
\begin{aligned}
w(RAM(a,d)) &= \sqrt{C(RAM(a,d))} \\
C_{RAM}(a,d) &= C_{RAMcell} \cdot (a+3) \cdot (d + loglog(d)) \\
C_{RAMcell} &= 2 \\
r &= \frac{w(RAM(a,d))}{d+1}
\end{aligned}
$$